

System is not functioning due to the deviation of frequency output is over the limit?

Step 2-1. If the frequency measured by frequency counter is over the limit, we should increase the external capacitance, C_L (or the values of C_d & C_g), to lower the frequency to our targeted frequency, vice versa.

Step 2-2. We could adopt a crystal with lower capacitance if the frequency is much higher than the targeted frequency, vice versa.